

**PATENT NUMBER**

U.S. **UTILITY** Patent Application

<p><b>W.H.</b></p> <p><b>O.I.P.E.</b></p> <p><b>SCANNED</b> <u>510</u> <b>Q.A.</b> <u>CC</u></p>	<p><b>PATENT DATE</b></p>
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APPLICATION NO. 09/613749	CONT/PRIOR F	CLASS 257	SUBCLASS 401	ART UNIT 2811	EXAMINER S RAO D. CHANDRAN
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ANT'S Satoshi Suzuki  
Tetsuo Kunii

Semiconductor device and process for manufacturing the same

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>  Amount Due: _____ Date Paid: _____	
	_____ (Date)		<b>ISSUE BATCH NUMBER</b>  _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		_____ (Date)	
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